WHAT IS CLAIMED IS:

1.   A method for generating a printed circuit board design module comprising:

compiling information that is usable to derive one of more low level details associated with a printed circuit board;

determining one or more high level constraints that are usable with the information to generate the low level details; and

generating the printed circuit board design module such that the printed circuit board design module includes the information and such that the printed circuit board design module is configured to receive the one or more high level constraints.

2.   The method of claim 1 further comprising:
generating a list of the low level details prior to compiling the information.

3.   The method of claim 1 wherein the high level constraints include schematic constraints.

4.   The method of claim 1 wherein the high level constraints include electrical constraints.

5.   The method of claim 1 wherein the high level constraints include mechanical constraints.

6.   The method of claim 1 wherein the high level constraints include cost constraints.

7.   The method of claim 1 wherein the low level details include routing details.

8.   The method of claim 1 wherein the low level details include component placement details.

9.   The method of claim 1 wherein the low level details include stack-up details.

10.   The method of claim 1 wherein the information includes mathematical equations usable to calculate the low level details using the high level constraints.

11.   The method of claim 1 wherein the information includes a table usable to determine the low level details using the high level constraints.

12.   The method of claim 1 further comprising:
generating the printed circuit board design module such that the printed circuit board design module is configured to receive the one or more high level constraints from a user interface incorporated into a schematic software tool.

13.   A computer system comprising:
a processor; and
a memory that includes a printed circuit board design module that is executable by the processor, the printed circuit board design module being generated by:
        compiling information that is usable to derive one of more low level details associated with a printed circuit board;
        determining one or more high level constraints that are usable with the information to generate the low level details; and
        generating the printed circuit board design module such that the printed circuit board design module includes the information and such that the printed circuit board design module is configured to receive the one or more high level constraints.

14.   The computer system of claim 13 wherein the information includes mathematical equations usable to calculate the low level details using the high level constraints.

15.   The computer system of claim 13 wherein the information includes a table usable to determine the low level details using the high level constraints.

16.   The computer system of claim 13 wherein the high level constraints are selected from the group consisting of schematic constraints, electrical constraints, and mechanical constraints.

17.   The computer system of claim 13 wherein the low level details are selected from the group consisting of routing details, component placement details, and stack up details.

18.   A computer-readable medium storing a printed circuit board design module executable by a computer system, where the printed circuit board design module is generated by:

    compiling information that is usable to derive one of more low level details associated with a printed circuit board;
    determining one or more high level constraints that are usable with the information to generate the low level details; and
    generating the printed circuit board design module such that the printed circuit board design module includes the information and such that the printed circuit board design module is configured to receive the one or more high level constraints.

19.   The computer-readable medium of claim 18 wherein the information includes mathematical equations usable to calculate the low level details using the high level constraints.

20.   The computer-readable medium of claim 18 wherein the information includes a table usable to determine the low level details using the high level constraints.

21.   The computer-readable medium of claim 18 wherein the high level constraints are selected from the group consisting of schematic constraints, electrical constraints, and mechanical constraints.

22.   The computer-readable medium of claim 18 wherein the low level details are selected from the group consisting of routing details, component placement details, and stack up details.